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** CONTINUING DATA *****

(None) LT

** FOREIGN APPLICATIONS *****

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LT

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature: <u>Chaitman</u> Initials: <u>LT</u>				

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TITLE

Wafer-level electronic modules with integral connector contacts and methods of fabricating the same

FILING FEE RECEIVED 1730	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____
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